



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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6-05-03

Group Art Unit: 2123

Examiner:

Applicant: J.G. Walacavage, et al.

Serial No.: 09/966,121

Filing Date: September 28, 2001

For: METHOD OF EMULATING MACHINE
TOOL BEHAVIOR FOR PROGRAMMABLE
LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM

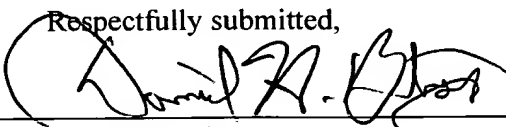
SUPPLEMENTAL
INFORMATION
DISCLOSURE STATEMENT

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Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

PTO Form-1449 is submitted herewith pursuant to the provisions of 37 CFR 1.97 and 1.98(a) as a means of complying with the requirements of 37 CFR 1.56 with respect to the above identified application. In accordance with Patent Office guidelines, copies of the citations listed on PTO-1449 are enclosed.

Respectfully submitted,
By: 
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CERTIFICATE OF MAILING: (37 C.F.R. 1.8) I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313- 1450 on May 19, 2003, by Daniel H. Bliss
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